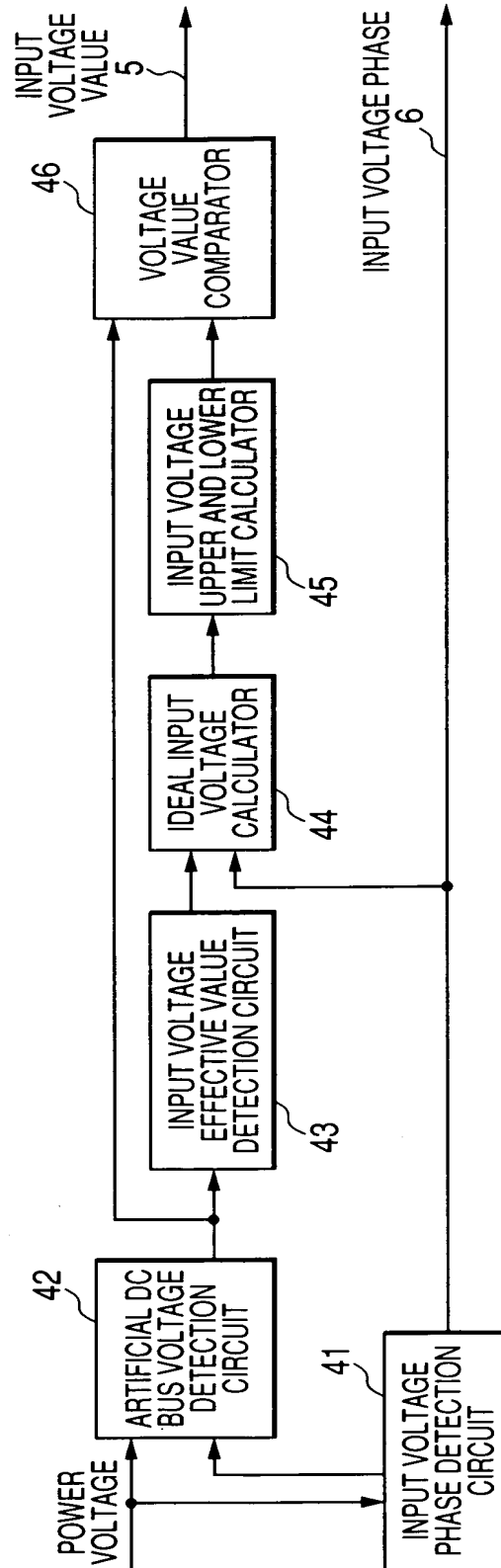


FIG. 2

DETAILED DIAGRAM FOR INPUT POWER VOLTAGE PHASE/MAGNITUDE DETECTOR 4



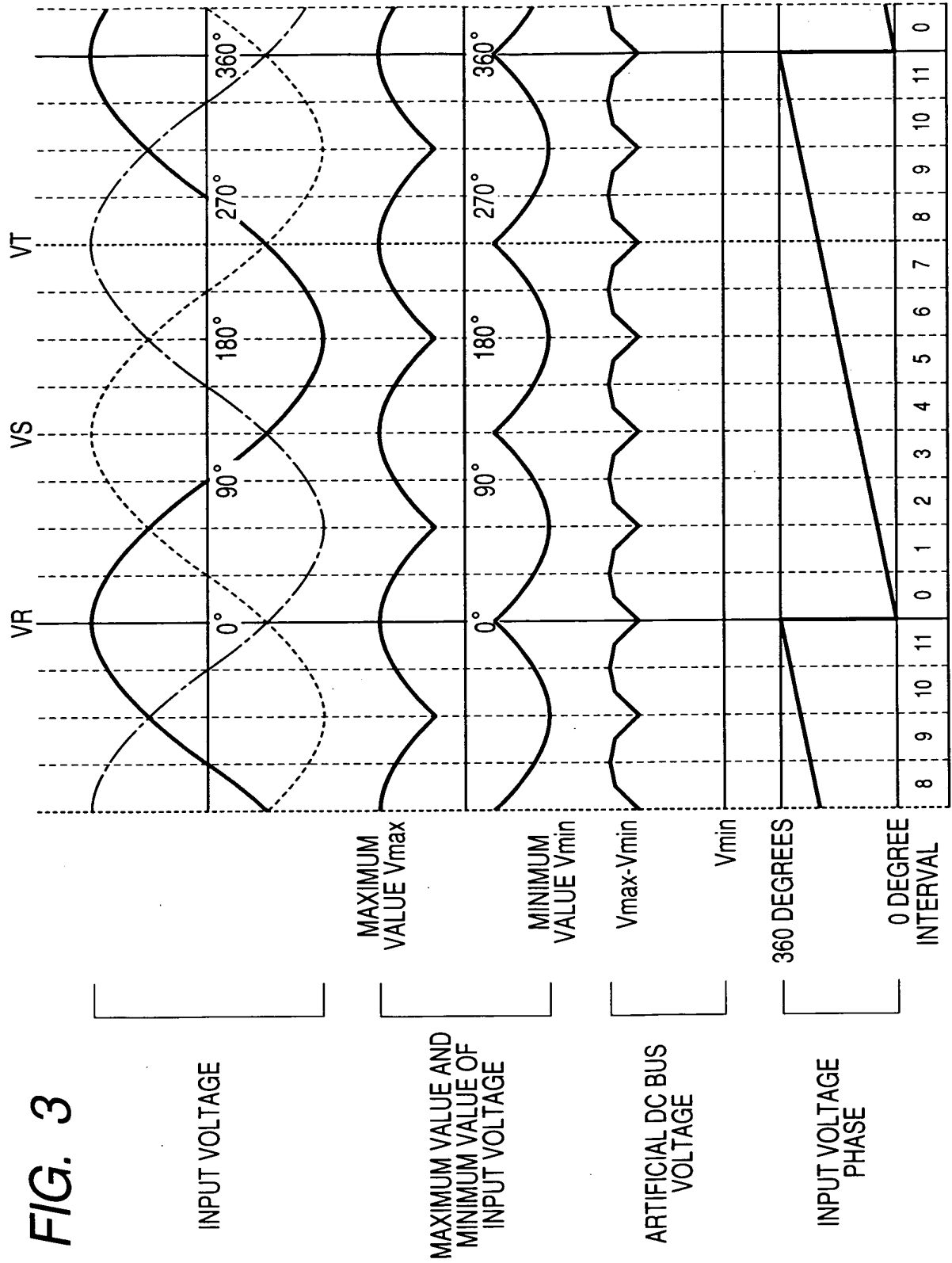


FIG. 4

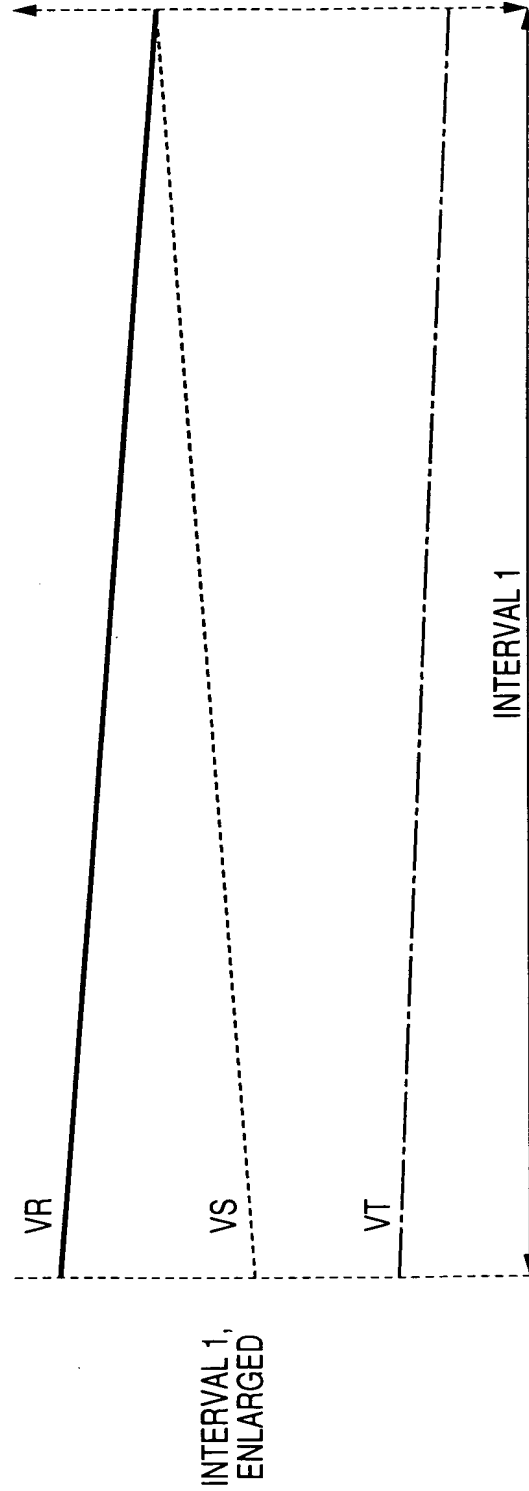


FIG. 5

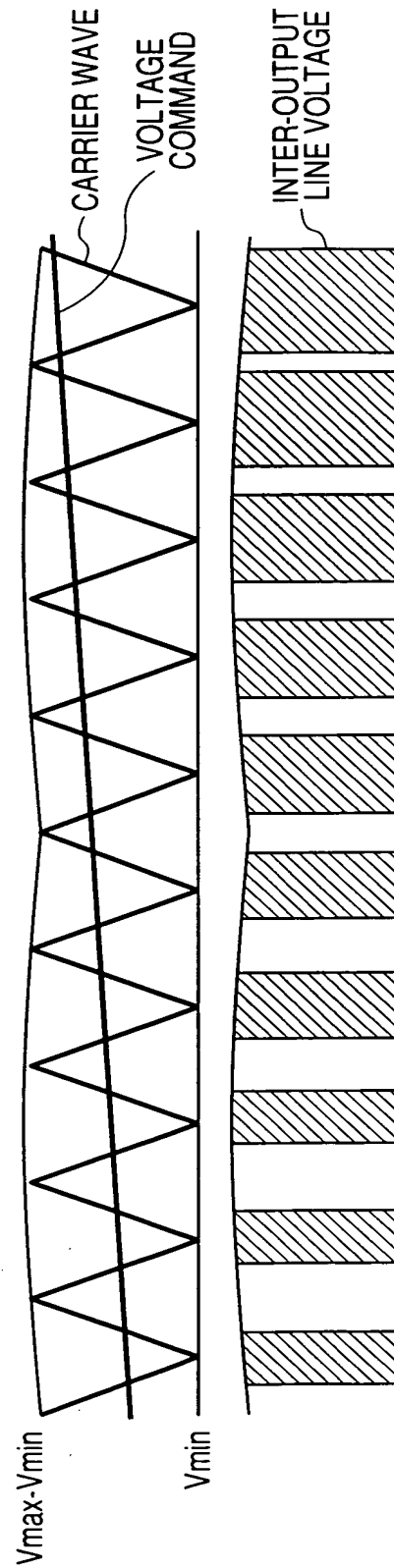
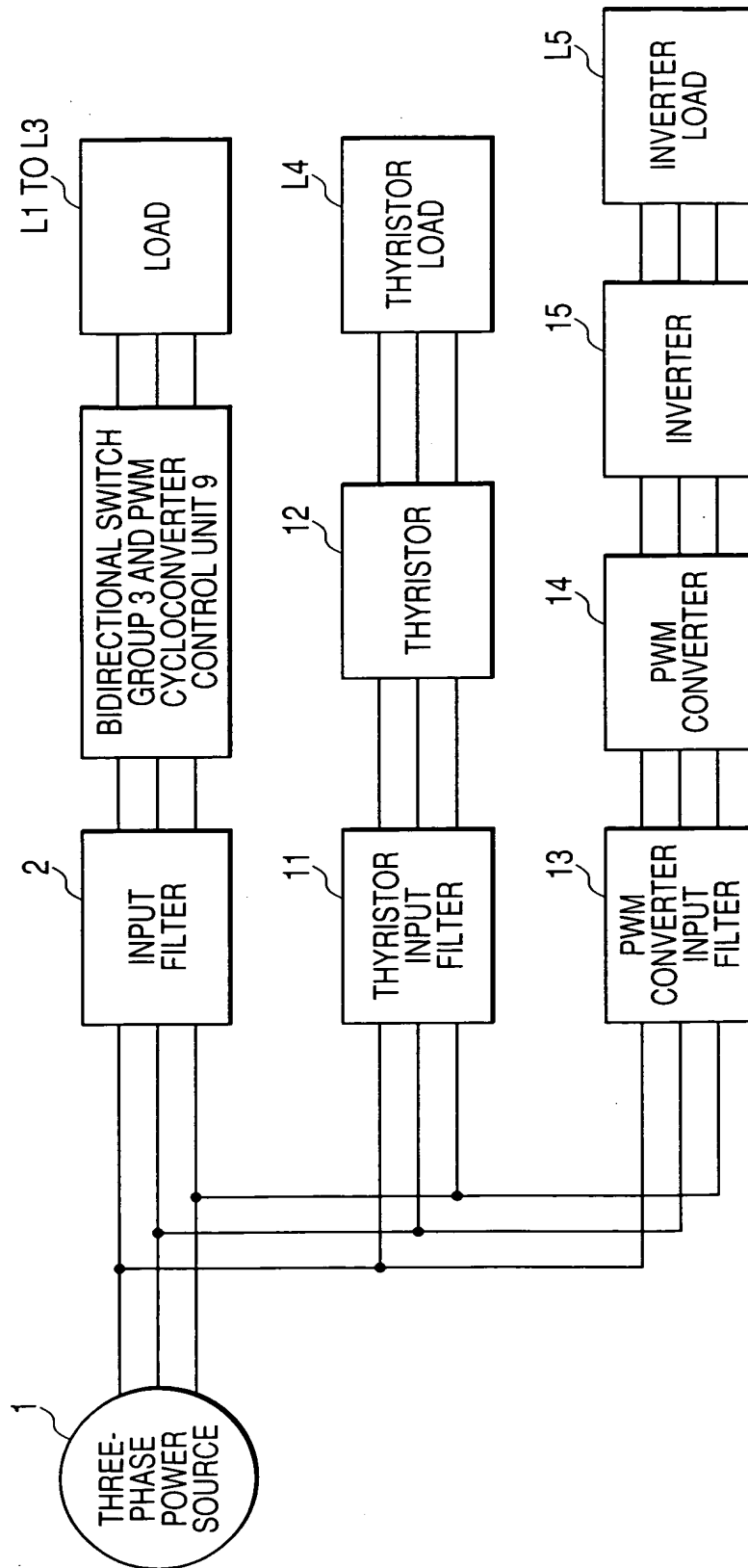


FIG. 6



**FIG. 7**

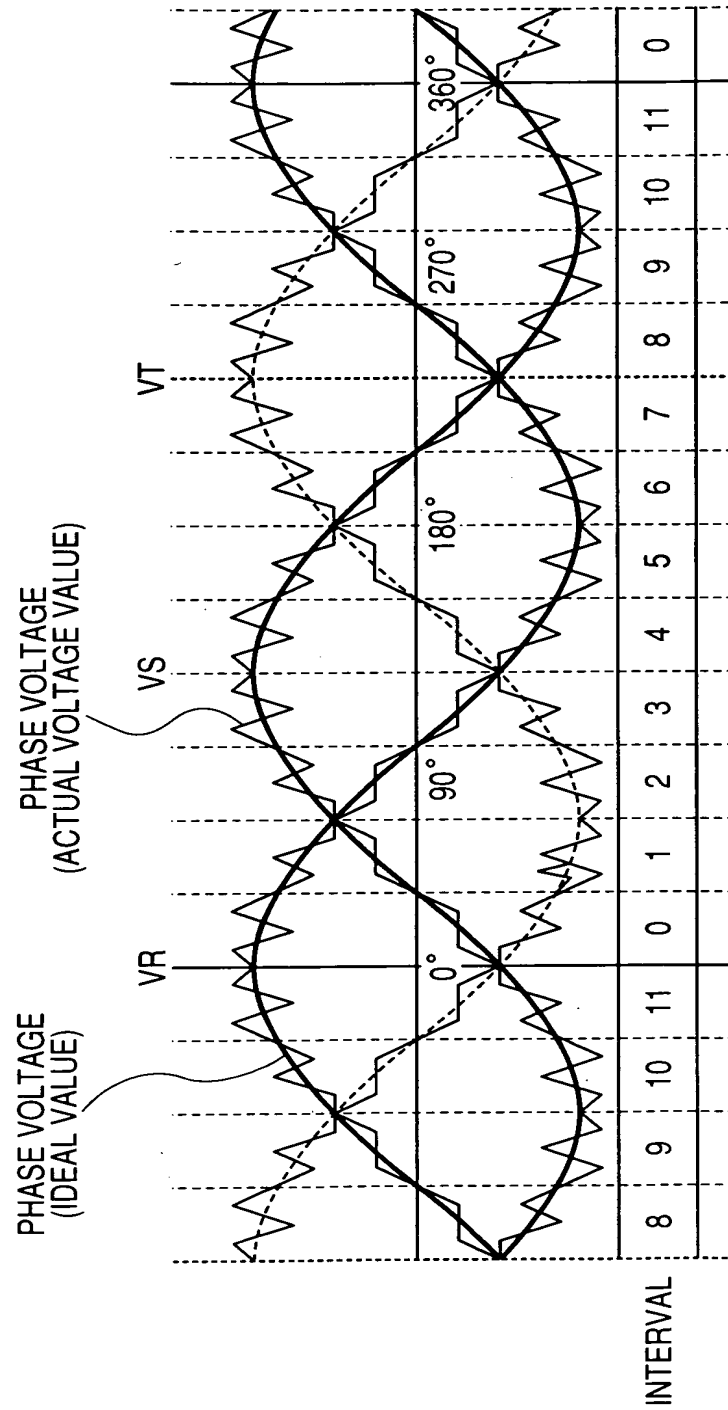


FIG. 8

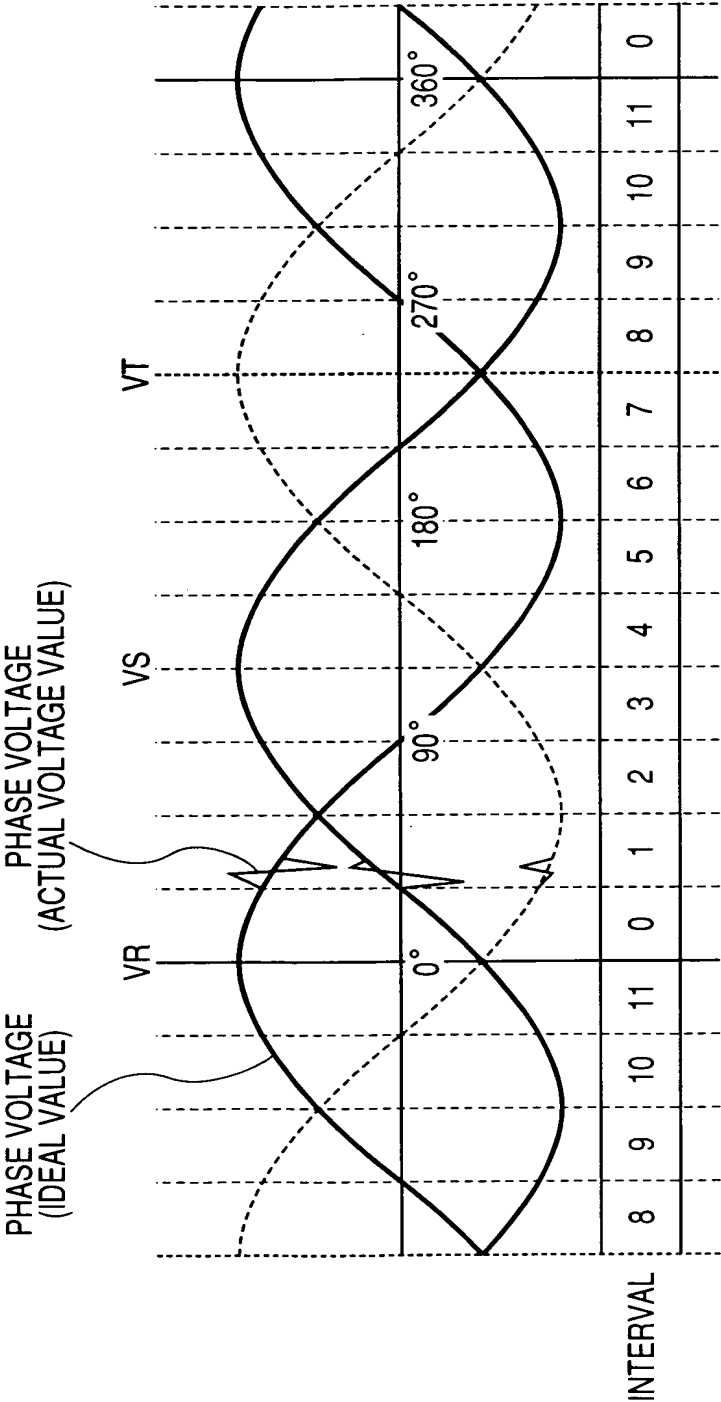




FIG. 9

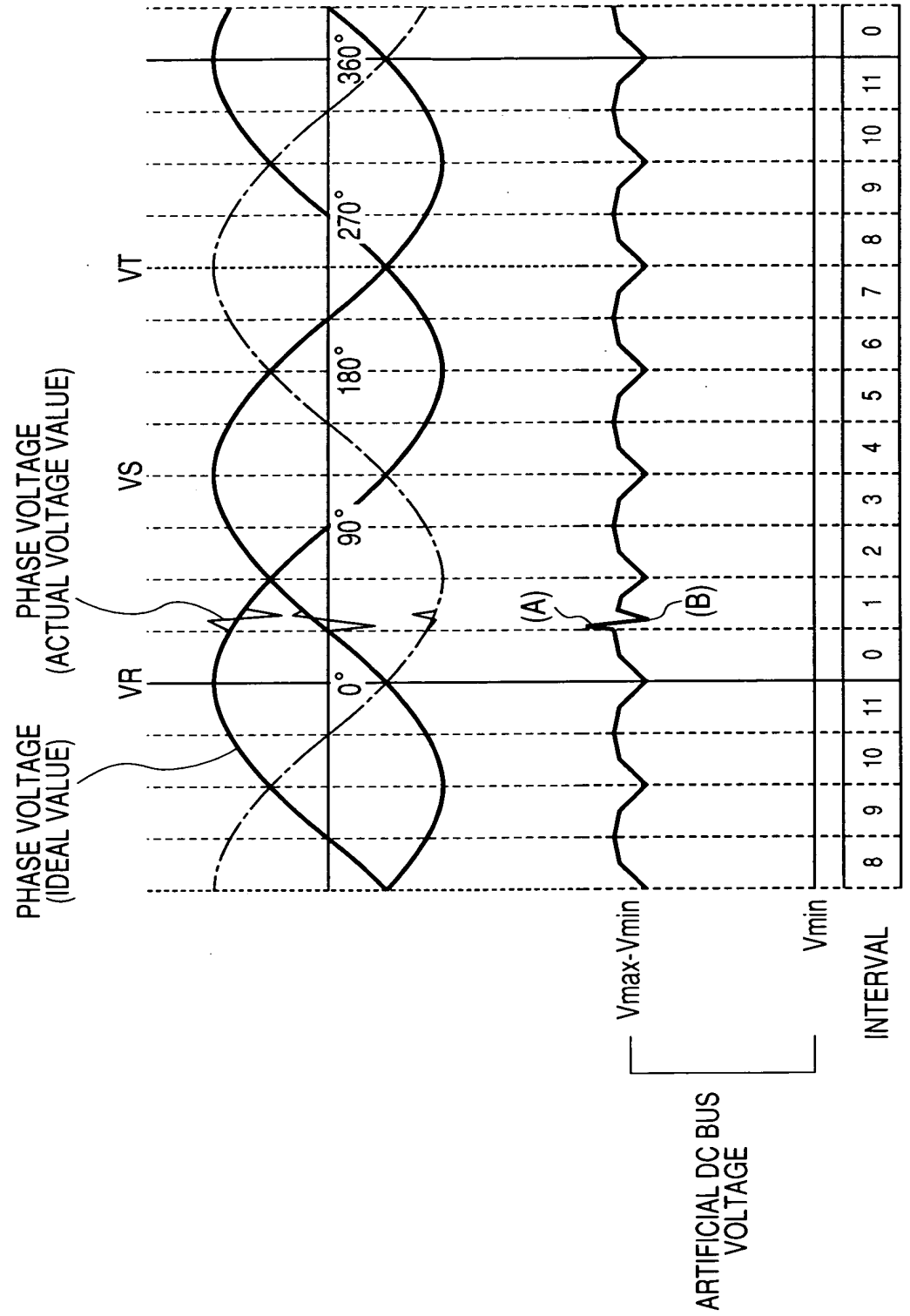
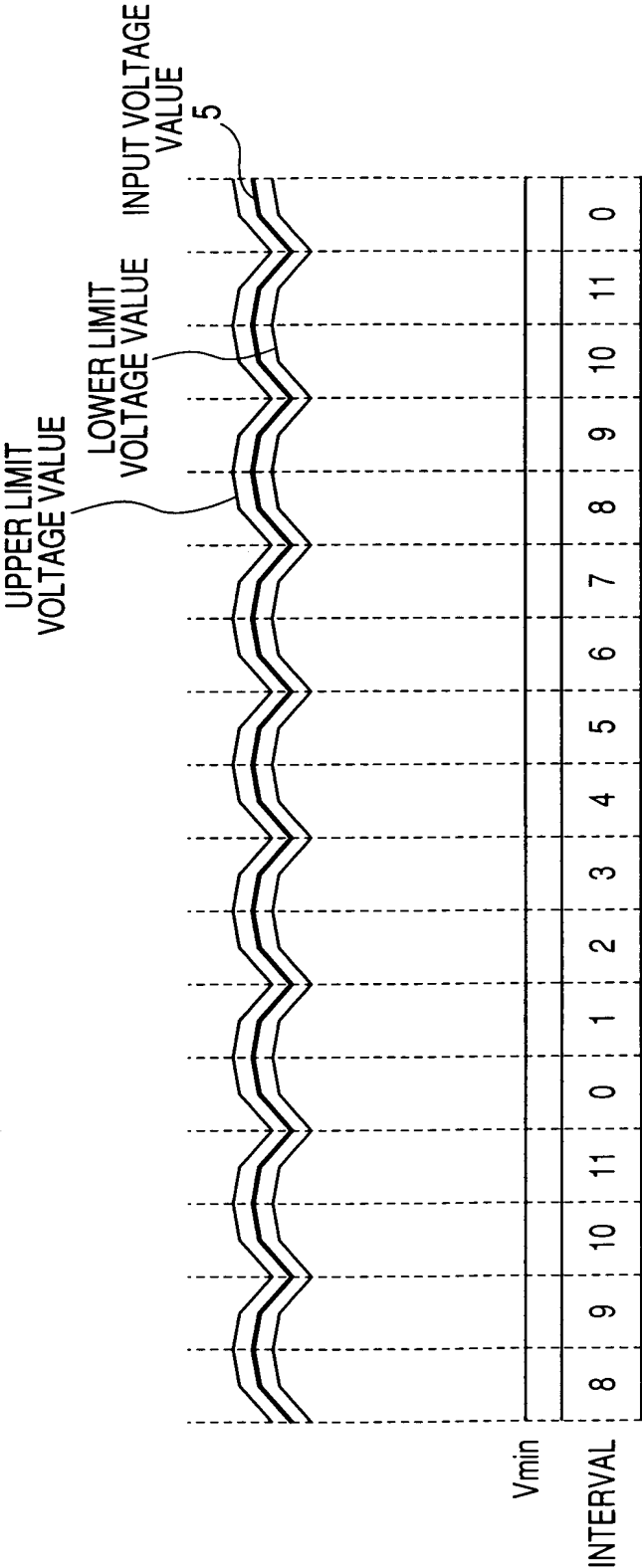


FIG. 10



**FIG. 11**

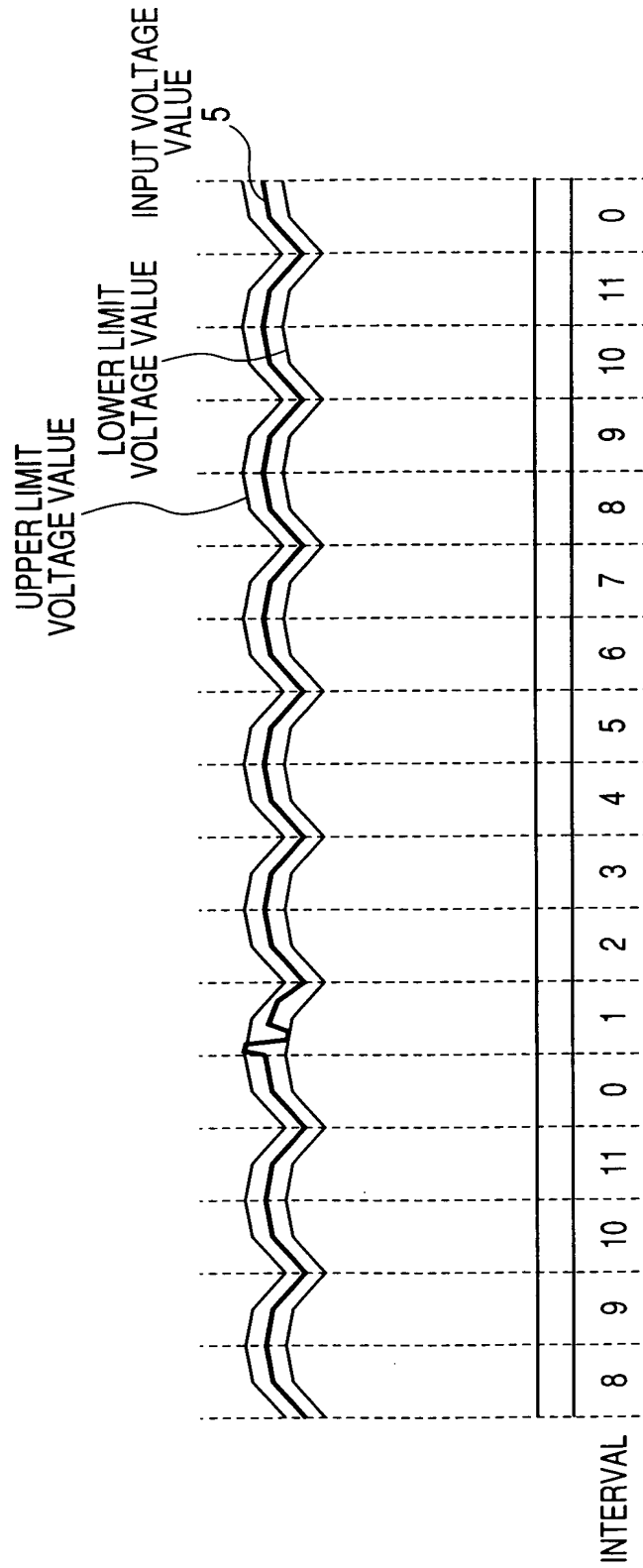


FIG. 12

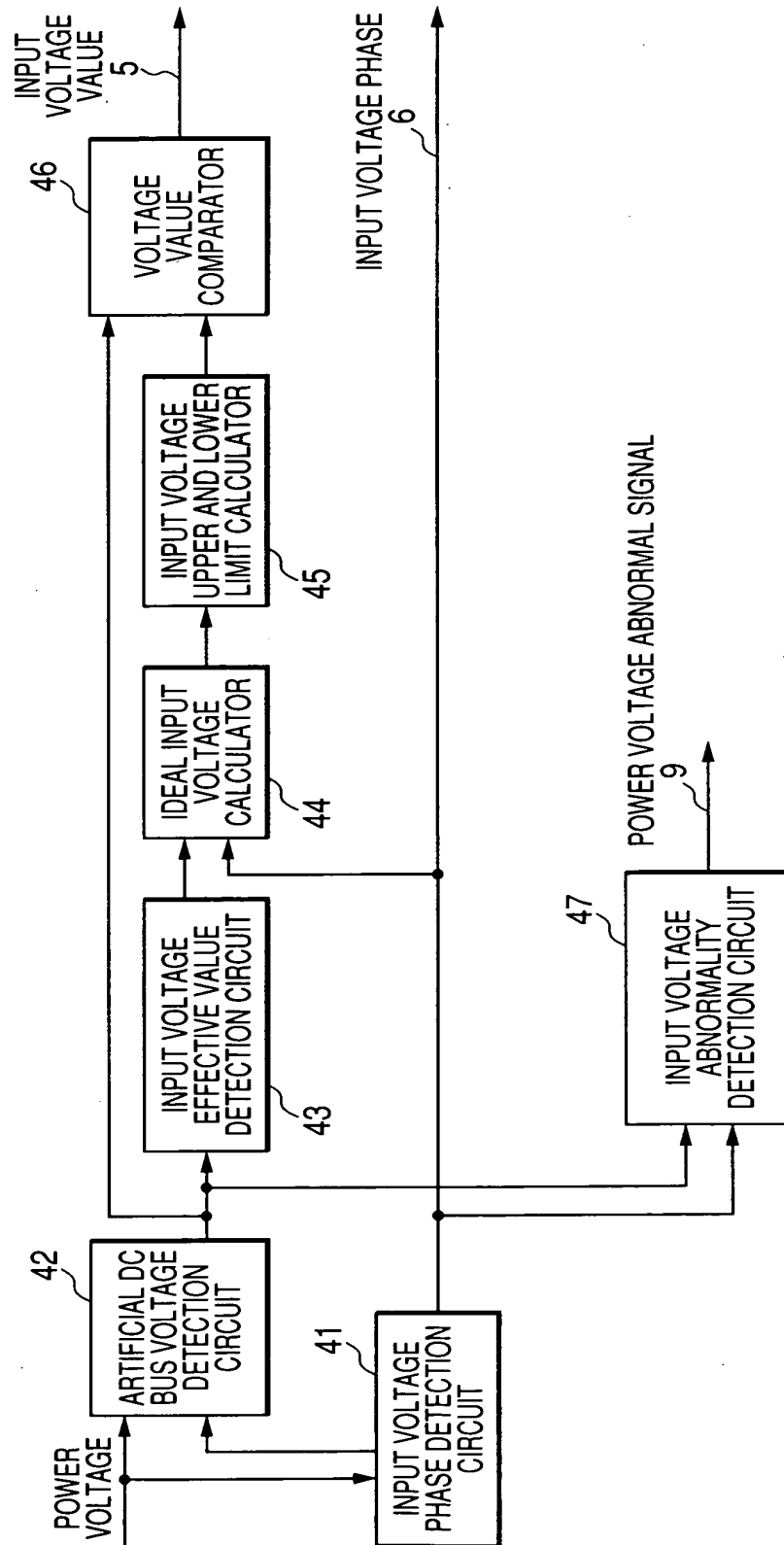
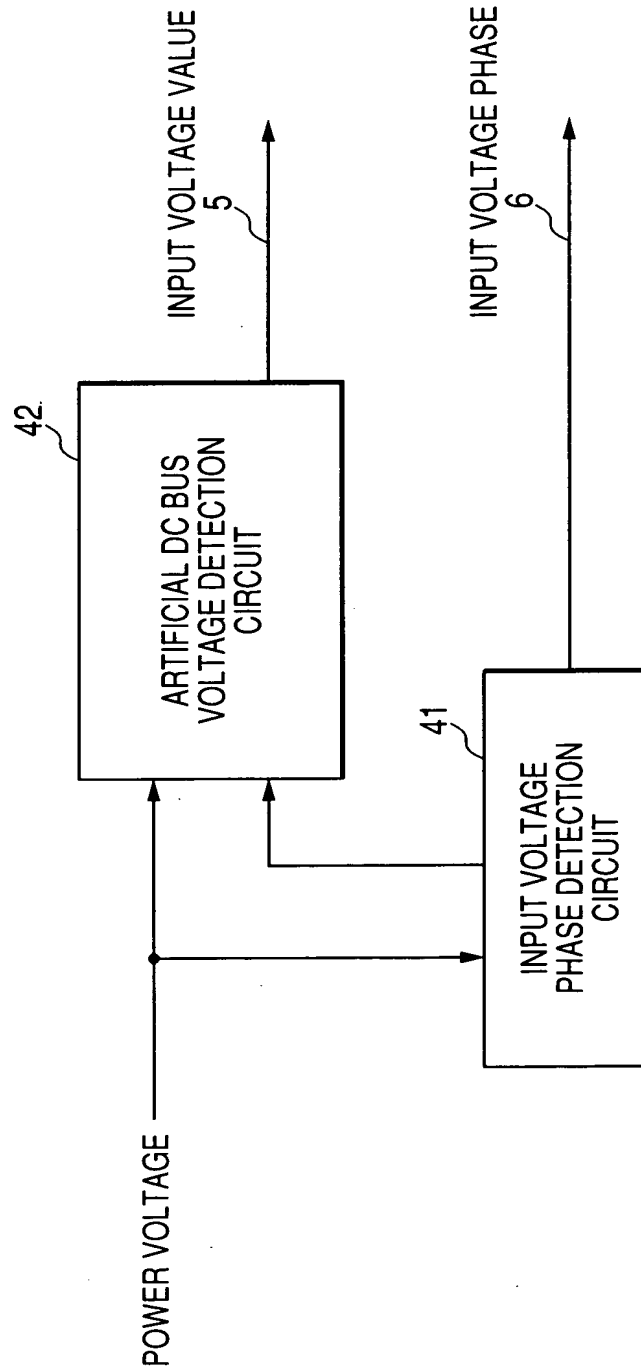


FIG. 13



**FIG. 14**

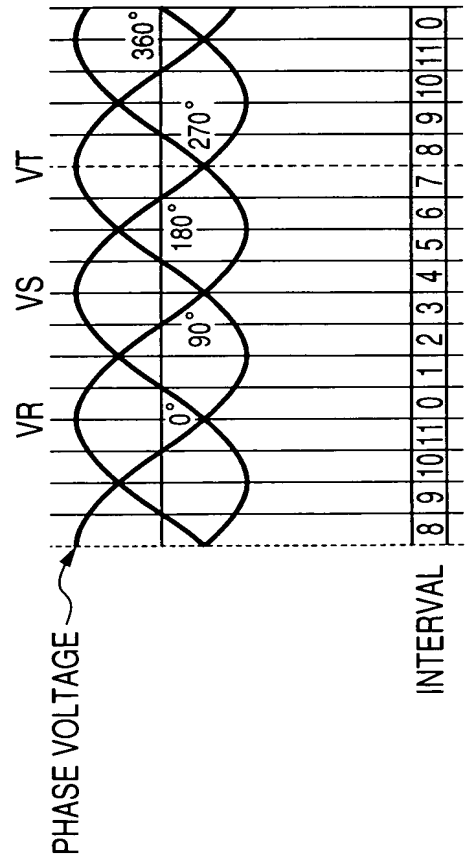
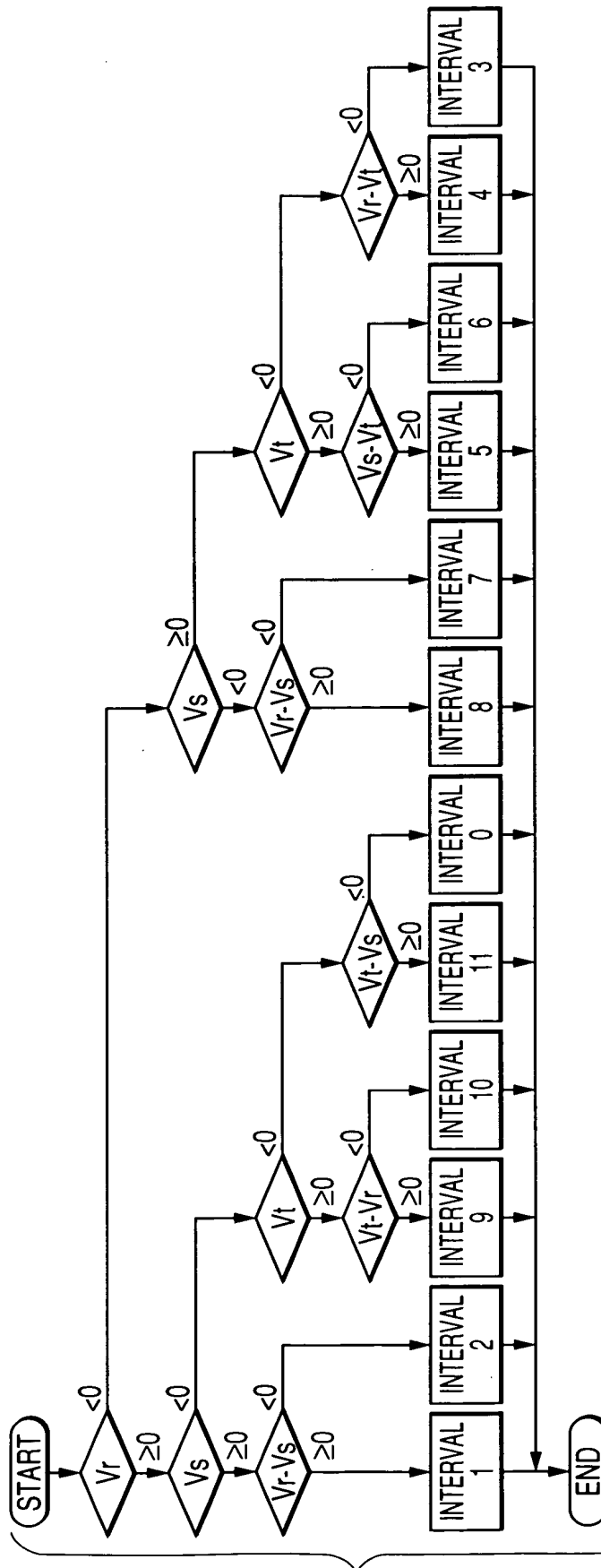


FIG. 15

